

Module - 5

Principles of spread spectrum

In any communication system, two important factors that are considered while designing are transmitter power & bandwidth.

→ Channel coding allows us to reduce the transmitter power by increasing the transmitted signal B.W.

Let R denote the information rate at the Tx & W denote the channel B.W.

→ The ratio W/R defines the BW expansion factor of channel coded transmitted signal, it is denoted by B_e .

→ By increasing B_e , the power in the transmitted signal is reduced to achieve required performance.

→ For example, in multiple access communication, when two or more transmitter uses same channel to transmit information, the interference created will effect the performance of the system.

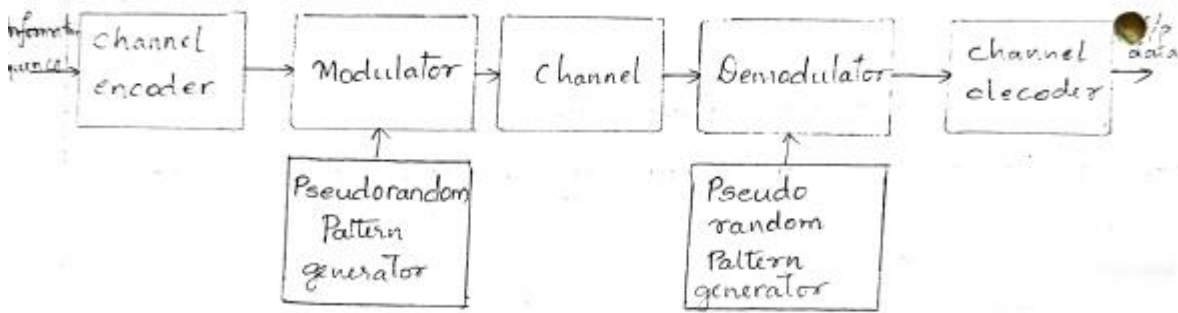
→ To overcome the degradation of performance caused by interference, the bandwidth of the transmitted signal is increase such that $B_e = W/R$ is much greater than unity. This is one of the characteristic of spread spectrum signal.

→ The second characteristics is that the information signal at the modulator is spread in bandwidth by means of a code that is independent of the information signal.

→ It is desired to have the system with minimum transmission B.W., there are situations where the transmission B.W. is purposefully high to a value much higher than baseband signal B.W., such

- techniques are called spread spectrum modulation.
- Main advantages of SSM are.
- * It provides immunity against intentional jamming of signals by other users.
 - * It provides immunity against interference from other channels.
 - * It also provides immunity against degradation of signal because of multipath fading.
 - * It provides asynchronous multiple access capability. Ex: CDMA.
- The definition of spread spectrum may be stated in two parts:
- * Spread spectrum is a means of transmission in which the data of interest occupies a BW in excess of the minimum BW necessary to send the data.
 - * The spectrum spreading is accomplished before transmission through the use of a code that is independent of the data sequence. The same code is used in the Rx^r to despread the received signal so that the original ip. data is reconstructed.
- Basically SSM is classified into two types based on code signal,
- * Direct sequence spread spectrum
 - * Frequency hopping spread spectrum.
- * Model of spread spectrum digital communication system:
- The basic block diagram is as depicted in fig(a).
- The basic elements of DCS are channel encoder, decoder, modulator & demodulator.

- In addition, a spread spectrum s/m. employs two identical pseudorandom sequence generators, one is interfaced with the modulator at the transmitting end & other with the demodulator at the receiving end.
- These two generators produce a pseudorandom (or) pseudonoise(PN) sequence, which is used to spread the transmitted signal at the modulator & to despread the received signal at the demodulator.



- Time synchronisation of the PN sequence generated at the receiver with the PN sequence contained in the received signal is required to properly despread the received spread spectrum signal.
- Synchronisation is established prior to the transmission of information, this is achieved by transmitting a fixed PN bit pattern.
- In the data mode, the communication system usually tracks the timing of the incoming received signal & keeps the PN sequence generator in synchronism.
- Interference is introduced in the transmission of the signal. & this interference is categorized as broadband (or) narrowband relative to the bandwidth of the information-bearing signal.

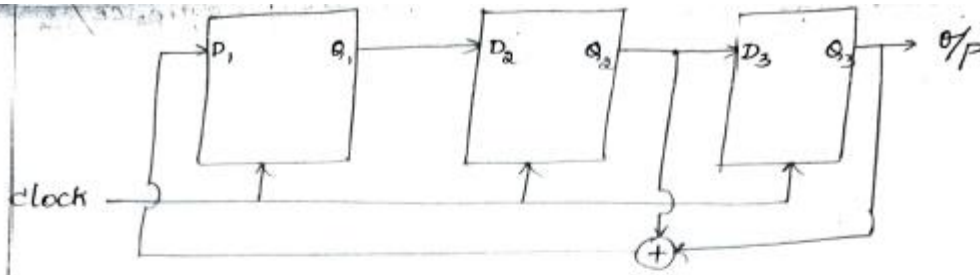
* Pseudo-noise sequence/Pseudo random sequence/

Maximum length sequence:-

- A PN sequence is a code sequence of 0's & 1's with certain autocorrelation property similar to those of white noise.
- The sequence used in spread spectrum communication is periodic. i.e. the sequence of 1's & 0's repeats periodically with known period.
- A shift register of length 'm' consists of m flipflops regulated by a single timing clock.
- Pseudo random signals are not random in nature.
- It is a deterministic periodic signal which will be known only to the Tx^T & Rx^T.
- It appears as random noise to unauthorized users, hence it is known as pseudonoise sequence (or) pseudo random binary sequence.
- PN sequence is generated by using m-stage shift register having appropriate feedback signal, with period " $N = 2^m - 1$ " bits, such sequences are called maximum length sequence.

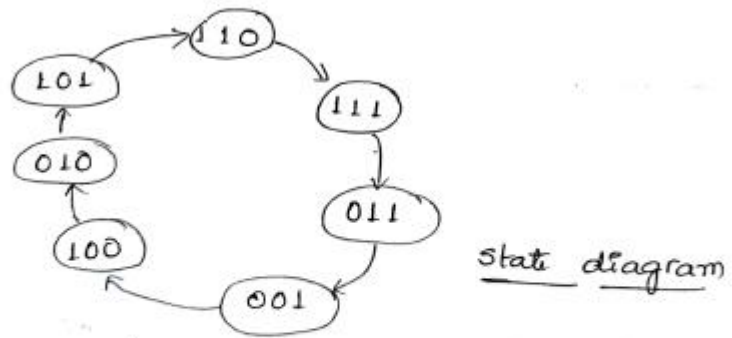
Where $m \rightarrow$ no of shift register

- At each clock pulse, the state of each flipflop is shifted 1 bit right. In order to prevent the shift register being empty by 'm' clock pulse, feedback is provided.
- The feedback function is obtained by using modulo-2 adder. The ip to modulo-2 adder are the output of various flipflop.
- For example:-
consider a 3-bit shift register with synchronous clocking which uses a linear feedback.



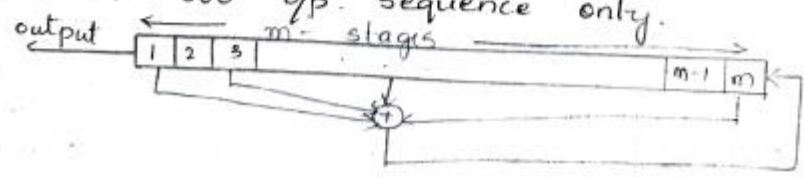
3-stage sequence generator

clock pulse	Present state	Next state
Initially	1 1 0	1 1 1
1	1 1 1	0 1 1
2	0 1 1	0 0 1
3	0 0 1	1 0 0
4	1 0 0	0 1 0
5	0 1 0	1 0 1
6	1 0 1	1 1 0
7	1 1 0	1 1 1



state diagram

'000' is not selected as initial status, because it results in "catastrophic cyclic code", i.e. it results in 000 o/p. sequence only.



* Properties of maximum length sequence:-

→ Three basic properties of ML sequence are,

- * Balance property
- * Runlength property
- * Correlation property

1. Balance property:-

Balance property states that, "In a periodic maximum length sequence the number of 1's is always one more than number of 0's.

$$\text{ie. No. of 1's} = 2^{m-1}$$

$$\text{No. of 0's} = 2^{m-1} - 1$$

$$\text{Period, } N = 2^m - 1$$

Ex: For a 3-stage shift register, $N = 2^3 - 1 = 7$

001011

$$\text{No. of 0's, } 2^{m-1} - 1 = 2^2 - 1 = 3$$

$$\text{No. of 1's, } 2^{m-1} = 2^2 = 4$$

2. Runlength property:-

Among the runs of 1's & 0's in each period of a ML-sequence, one half the runs are of length one, one-fourth are of length two, one eighth are of length three & so on as long as these fractions represent meaningful no of runs.

→ For a ML-sequence generated by feedback shift register of length 'N', then the total number of runs is $\frac{N+1}{2}$

→ Ex: 001011, $N = 7$

$$\text{Total no. of runs} = \frac{N+1}{2} = \frac{7+1}{2} = 4$$

00, 1, 0, 111 = 4 runs

10 → two runs one of length one

00 → one run is of length two

111 → one run is of length three

3 Correlation property :-

- The autocorrelation function of a M_k -sequence is periodic & binary valued.
- Let binary symbols '0' & '1' represent '-1' & '+1' ∴ the autocorrelation sequence of a binary sequence $\{c_n\}$ is given by,

$$R_c(m) = \sum_{n=1}^N c_n c_{n+m} ; 0 \leq m \leq N-1$$

Where, N - period of the sequence.

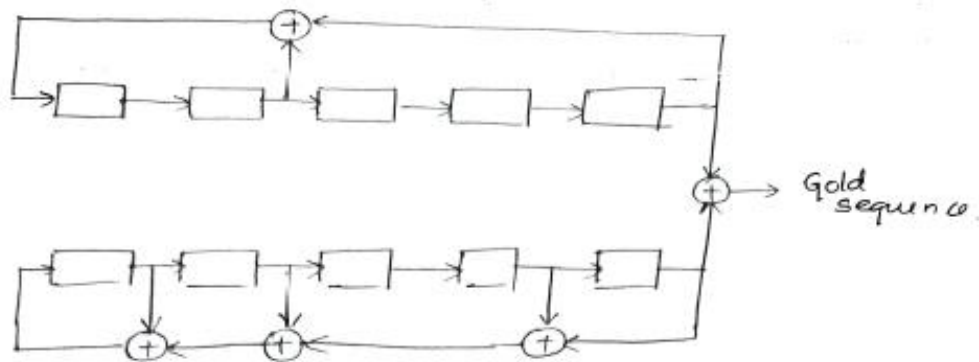
Since c_n is periodic with period N , the autocorrelation sequence R_c is also periodic with period N .

- The ideal autocorrelation sequence is,

$$R_c(m) = \begin{cases} N & m=0 \\ -1 & 1 \leq m \leq N-1 \end{cases}$$

- Methods for generating PNI sequences with better periodic cross correlation properties have been developed by Gold & Kasami.
- Gold sequence is generated by taking a pair of specially selected m-sequences called preferred m-sequences & forming the modulo-2 sum of two sequences for each of N cyclicly shifted versions one sequence relative to the other sequence.

→ N gold sequences are generated as illustrated in fig 1a.



Generation of gold sequence of length 31

→ For m odd, the maximum value of the cross correlation function b/w any pair of gold sequence is,

$$R_{\max} = \sqrt{2L}$$

→ For m even, $R_{\max} = \sqrt{L}$

→ Kasami described a method of constructing PN sequence by decimating an m-sequence.

→ In this method, every $2^{m/2} + 1$ bit of an m-bit sequence is selected.

→ This method yields smaller set of PN sequence compared to gold sequence, with maximum cross correlated value is $R_{\max} = \sqrt{L}$

1. Verify all the properties & find the period of PN sequence 01011100101110 [3-stage shift register]

→ sol: $m=3$.

$$\therefore N = 2^m - 1 = 2^3 - 1 = 7$$

$$\underbrace{0101110}_{7 \text{ bits}} \quad | \quad 0101110$$

* Balance property :-

$$\text{No. of 1's: } 2^{m-1} = 2^2 = 4$$

$$\text{No. of 0's: } 2^{m-1} - 1 = 2^2 - 1 = 3.$$

* Runlengths property :- Total No of runs, $\frac{N+1}{2} = 4$

No. of runs according to given sequence is 5, but it should be 4. \therefore In order to make it as 4 runs shift one bit right, \therefore we get

00, 1, 0, 111

\therefore Total runs = 4

1, 0 \rightarrow two runs are of length one

00 \rightarrow one run is of length two

111 \rightarrow one run is of length three.

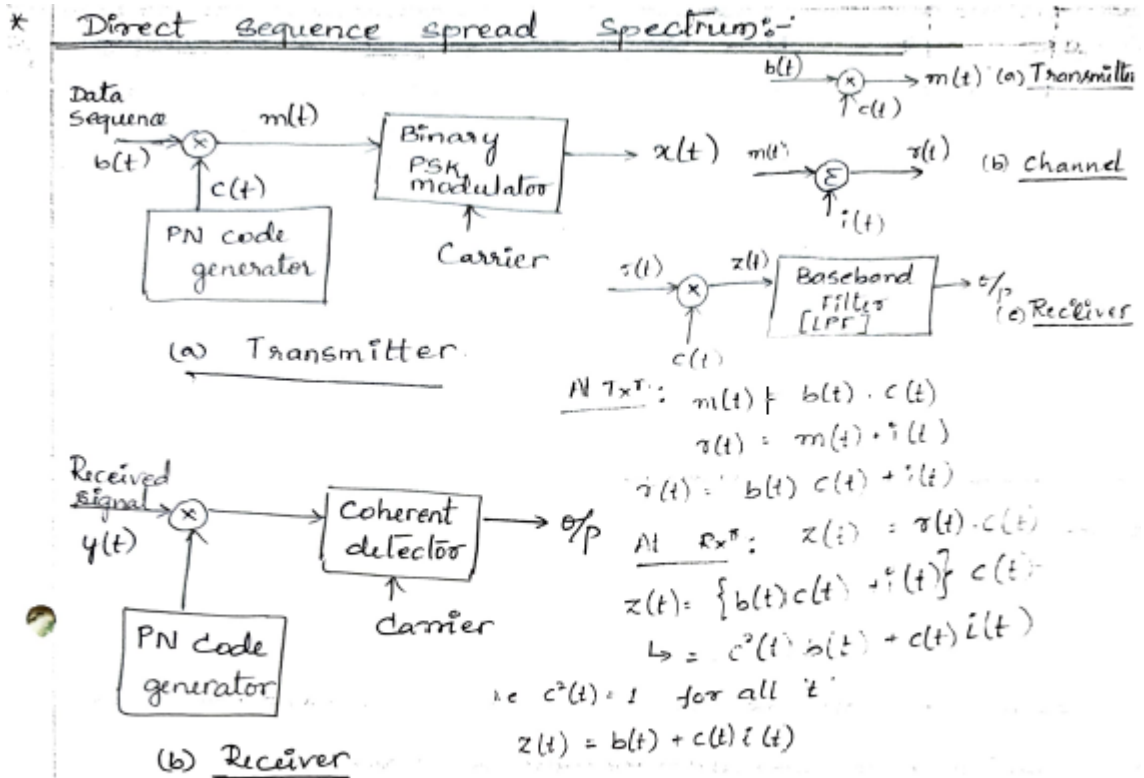
* Autocorrelation property:

$$\begin{array}{r}
 C_n \quad 0 \quad 1 \quad 0 \quad 1 \quad 1 \quad 1 \quad 0 \\
 \text{bits } C_n \quad -1 \quad +1 \quad -1 \quad +1 \quad +1 \quad +1 \quad -1 \\
 C_n \quad -1 \quad +1 \quad -1 \quad +1 \quad +1 \quad +1 \quad -1 \\
 \hline
 \quad \quad 1 \quad 1 \quad 1 \quad 1 \quad 1 \quad 1 \quad 1 = 7
 \end{array}$$

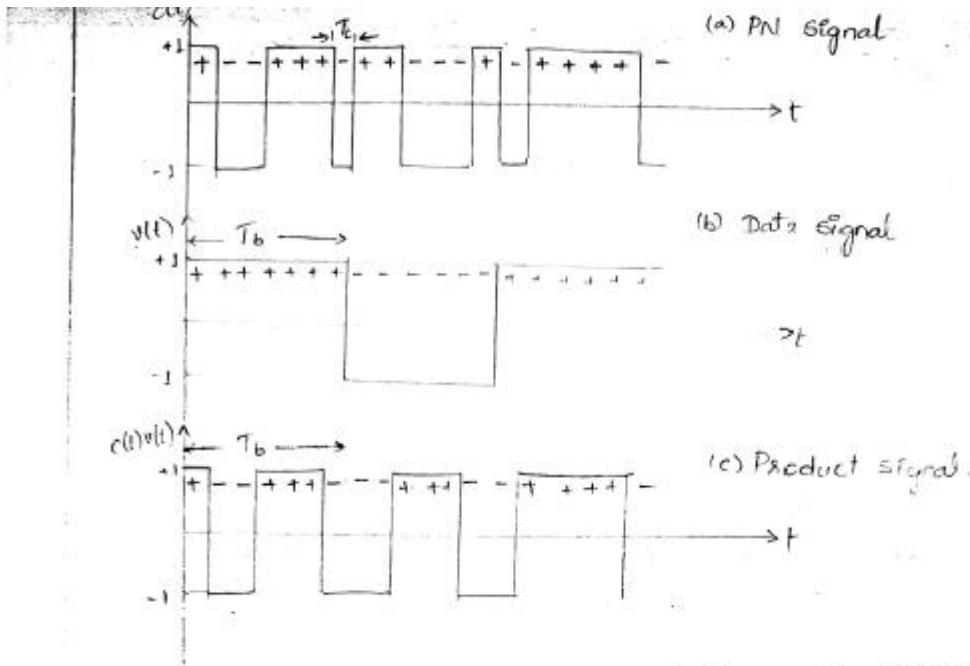
$$\begin{array}{r}
 C_n \quad -1 \quad +1 \quad -1 \quad +1 \quad +1 \quad +1 \quad -1 \\
 C_{n-1} \quad -1 \quad -1 \quad +1 \quad -1 \quad +1 \quad +1 \quad +1 \\
 \hline
 \quad \quad \cancel{+1} \quad \cancel{+1} \quad -1 \quad -\cancel{+1} \quad \cancel{+1} \quad \cancel{+1} \quad -\cancel{+1} = -1
 \end{array}$$

$$\begin{array}{r}
 C_n \quad -1 \quad +1 \quad -1 \quad +1 \quad +1 \quad +1 \quad -1 \\
 C_{n-2} \quad +1 \quad -1 \quad -1 \quad +1 \quad -1 \quad +1 \quad +1 \\
 \hline
 \quad \quad -1 \quad -\cancel{+1} \quad \cancel{+1} \quad \cancel{+1} \quad -\cancel{+1} \quad \cancel{+1} \quad -\cancel{+1} = -1
 \end{array}$$

$$\therefore R_c(m) = \begin{cases} N=7 & m=0 \\ -1 & 1 \leq m \leq N-1 \end{cases}$$



- Consider the transmission of a binary information sequence by means of binary PSK.
- The information rate is R bits per second & the bit interval is $T_b = \frac{1}{R}$ seconds. The available channel bandwidth is B_c Hz, where $B_c \gg R$.
- At the modulator, the bandwidth of the information signal is expanded to $W = B_c$ Hz by shifting the phase of the carrier pseudorandomly at a rate of W times per second according to the pattern of the PN-generator.
- DSSS technique can be used over bandpass channel by incorporating coherent BPSK into Tx & Rx.
- The basic method of accomplishing the spreading is shown in fig. (c).
- The information signal $v(t)$ is, $v(t) = \sum_{n=-\infty}^{\infty} a_n g_T(t - nT_b) \rightarrow \infty$
 Where, $a_n = \pm 1$ $-\infty < n < \infty$.
 $g_T(t) \rightarrow$ rectangular pulse of duration T_b .



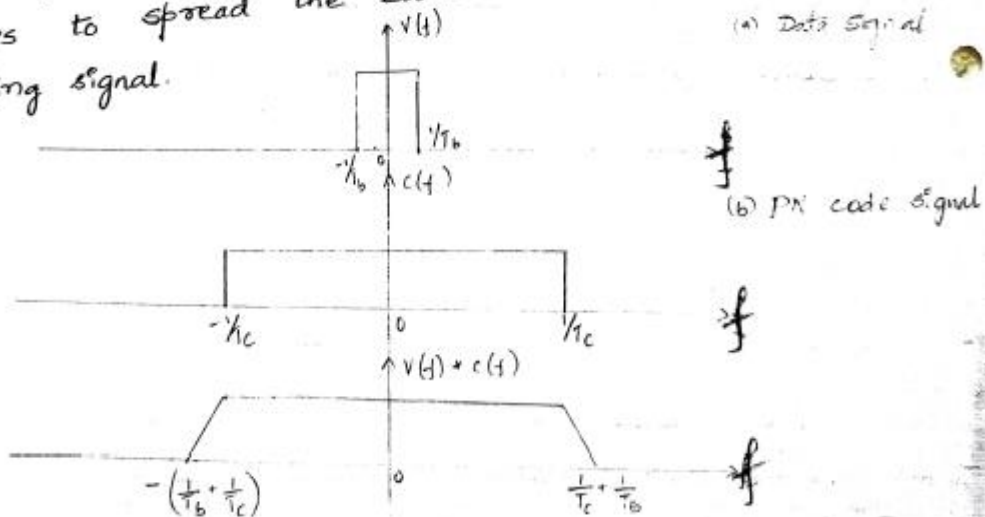
→ $v(t)$ is multiplied with the PN sequence generated by PN sequence generator, the PN sequence is expressed as,

$$c(t) = \sum_{n=-\infty}^{\infty} C_n p(t - nT_c) \rightarrow (2)$$

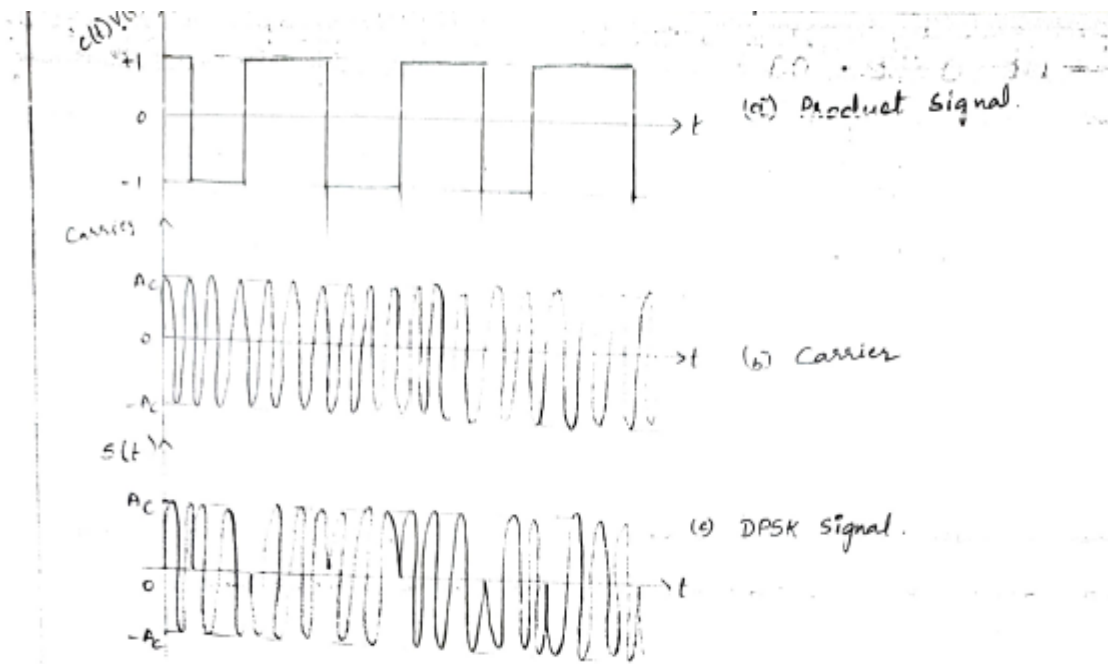
Where, $C_n \rightarrow$ binary PN sequence of ± 1

$p(t) \rightarrow$ rectangular pulse of duration T_c . It is called chip $[T_c]$ multiplication operation

→ $v(t)$ & $c(t)$ is multiplied. This spreads the bandwidth of the information bearing signal.



Convolution of the spectra.



→ The product signal $v(t)c(t)$ amplitude modulates the carrier & generates DSB-SC, $u(t) = A_c v(t) c(t) \cos 2\pi f_c t$.

Since, $v(t) c(t) = \pm 1$, $\forall t$, it follows that the carrier modulated signal can also be expressed as,

$$u(t) = A_c \cos [2\pi f_c t + \theta(t)].$$

Where, $\theta(t) = 0$, when $v(t) c(t) = 1$
 & $\theta(t) = \pi$, when $v(t) c(t) = -1$

\therefore the transmitted signal is BPSK signal.

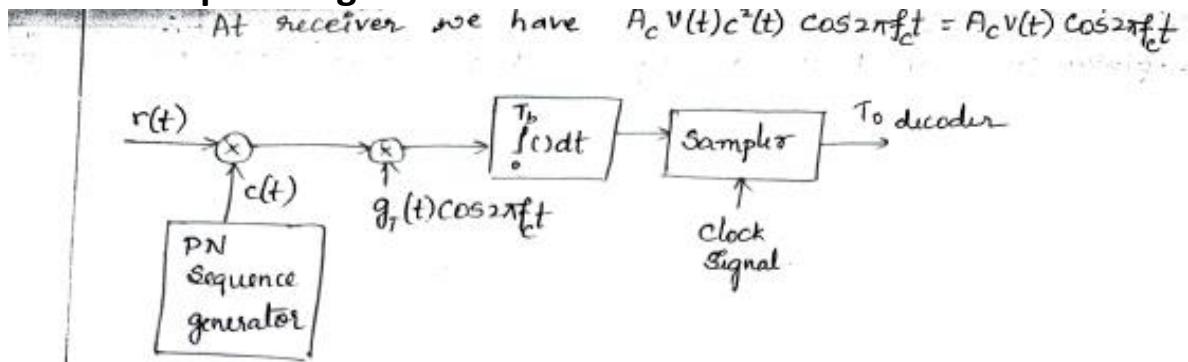
→ The rectangular pulse $p(t)$ is called chip, with duration T_c called chip interval. The ratio of T_b and T_c is denoted by k_c .

$$k_c = \frac{T_b}{T_c} \quad \text{where, } k_c \rightarrow \text{no of chips of PN sequence per information bit.}$$

→ k_c also represents no of possible 180 phase transitions in the transmitted signal.

→ The block diagram of demodulation is shown in fig(1). The received signal is first multiplied by $c(t)$, this is called despreading.

Effect of de-spreading:



Fig(1): Demodulation of DSSS signal

* DSSS System performance:-

The performance of DSSS is evaluated based on,

- * processing gain
- * Probability of error
- * Jamming margin

* Processing gain:-

The ratio of bit interval to chip interval is called processing gain

- * The total power in the interference at the output of the demodulator is,

$$I_0 R_b = \frac{P_I R_b}{W} = \frac{P_I}{W/R_b} = \frac{P_I}{T_b/T_c} = \frac{P_I}{L_c}$$

where, $I_0 = \frac{P_I}{W}$ Power spectral density of wideband interference.

and, $P_I = \frac{A_I^2}{2}$ is the average power of the interference.

- * The power in the interfering signal is reduced by an amount equal to the bandwidth expansion factor $W/R_b = T_b/T_c = L_c$.

- * The ratio of bandwidth of spreaded message signal to the bit rate of unspreaded data signal.

$$P.G = \frac{\text{B.W. of spreaded signal}}{\text{Bit rate of unspreaded signal}} = \frac{W_c}{R_b}$$

where, BW of spreaded signal, $W_c = \frac{1}{T_c}$
 Bit rate of unspreaded signal, $R_b = \frac{1}{T_b}$

$$P.G = \frac{W_c}{R_b} = \frac{1/T_c}{1/T_b} = \frac{T_b}{T_c} = L_c$$

W.K.T, $T_b = NT_c$

$$\therefore P.G = \frac{NT_c}{T_c} = N$$

* Probability of error :-

The probability of error of DSSS is given by,

$$P_e = Q \left[\sqrt{\frac{2E_b}{I_0}} \right]$$

$$\rightarrow \begin{cases} Q(z) = \frac{1}{2} \operatorname{erfc} \left[\frac{z}{\sqrt{2}} \right] \\ Q(z) = \int_z^{\infty} \frac{1}{\sqrt{2\pi}} e^{-\frac{y^2}{2}} dy \end{cases}$$

Where, $I_0 \rightarrow$ power spectral density of an equivalent broadband interference

Q. A DSSS signal is designed so that the power ratio P_R/P_N at the intended receiver is 10^{-2} . If the desired $E_b/N_0 = 10$ for acceptable performance, determine the minimum value of the processing gain.

\rightarrow Solⁿ: $\left(\frac{P_R}{P_S} \right)_{dB} = 20dB; \quad \frac{P_I}{P_S} = 100$

$$(SNR)_D = 13dB = 20$$

$$\therefore L_c = \frac{1}{2} \left[\frac{P_I}{P_S} \right] (SNR)_D = \frac{1}{2} \times 100 \times 20 = 1000$$

* Interference margin :- is a measure of immunity against Jamming.

Eqⁿ (1) can be expressed as,

$$\frac{E_b}{I_0} = \frac{P_S T_b}{P_I / W} = \frac{P_S / R_b}{P_I / W} = \frac{W / R_b}{P_I / P_S}$$

$$\text{or } 10 \log \left(\frac{E_b}{I_0} \right) = 10 \log \left(\frac{W}{R_b} \right) - 10 \log \left(\frac{P_I}{P_S} \right)$$

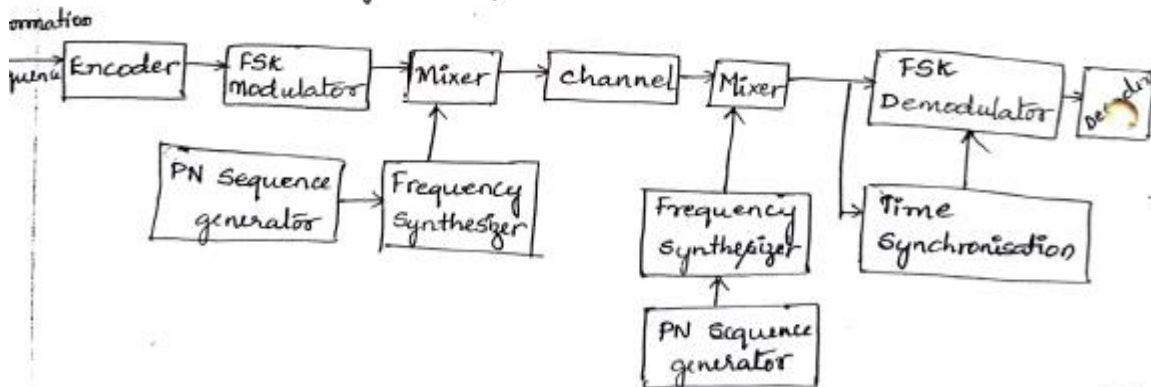
$$\text{or } 10 \log \left(\frac{P_S}{P_S} \right) = 10 \log \left(\frac{W}{R_b} \right) - 10 \log \left(\frac{E_b}{I_0} \right)$$

$$\left(\frac{P_I}{P_S}\right)_{dB} = \left(\frac{W}{R}\right)_{dB} - \left(\frac{E_b}{I_0}\right)_{dB}$$

The ratio $\left(\frac{P_I}{P_S}\right)_{dB}$ is called interference margin.

* Frequency hopping spread spectrum :-

- Frequency hopping spread spectrum is a type of spread spectrum in which the carrier hops from one frequency to another.
- In FHSS, the available ^{channel} bandwidth 'W' is subdivided into a large number of non-overlapping frequency slots.
- In any signaling interval, the transmitted signal occupies one (or) more of the available frequency slots.
- The selection of the frequency slot in each signal interval is made pseudorandomly according to the o/p. of PN generator.
- The block diagram of FHSS is shown below,



- The modulation used is either binary or M-ary FSK
- If BFSK is used, the modulator selects one of two freq. 'f₀' (or) 'f₁' corresponding to the transmission of a '0' (or) a '1'.
- The resulting BFSK signal is translated in frequency by an amount determined by the o/p. sequence

- freq. f_c that is synthesized by the frequency synthesizer.
- This frequency is mixed with the op. of FSK modulator & the resultant signal is transmitted over the channel.
 - By taking 'm' bits from the PN generator, $2^m - 1$ possible carrier frequencies can be specified.
 - At the receiver, there is an identical PN sequence generator which is synchronized with the received signal & is used to control the op. of the frequency synthesizer.
- Thus, the pseudorandom frequency translation introduced at the Tx^r is removed at the Rx^r.
- The resultant signal is demodulated using FSK demodulator.
 - The frequency hopping rate, denoted as R_h , may be either equal to the symbol rate, lower than the symbol rate or higher than the symbol rate.
 - Based on frequency hopping rate, FHSS is classified into 2 types:
 - * Slow frequency hopping s.s: R_h is equal to or lower than symbol rate.
[$R_h \leq R_b$]
 - * Fast frequency hopping s.s: R_h is higher than the symbol rate.
[$R_h > R_b$]
 - FHSS signals can be used in CDMA where many users share a common bandwidth
 - * Slow frequency hopping:-
Consider a slow frequency hopping system in which the hop rate $R_h = 1$ hop per bit.
 - Assume the interference on the channel is

→ ∴ The probability of error for the detection of noncoherently demodulated BFSK is,

$$P_e = \frac{1}{2} e^{-P_b/2}$$

where, $P_b = \frac{E_b}{I_0}$ is the SNR per bit.

$$(or) P_e = \frac{1}{2} e^{-\frac{E_b}{2I_0}}$$

$$But, E_b = P_s T_b = \frac{P_s}{R_b}$$

where, $P_s \rightarrow$ avg. transmitted power
 $R_b \rightarrow$ bit rate

$$\& I_0 = \frac{P_I}{W}$$

$P_I \rightarrow$ avg. power of the broadband interference
 $W \rightarrow$ channel bandwidth

$$\therefore P_b = \frac{E_b}{I_0} = \frac{P_s \cdot W}{P_I \cdot R_b} = \frac{W/R_b}{P_I/P_s}$$

where, $W/R_b \rightarrow$ processing gain

$P_I/P_s \rightarrow$ interference margin.

Ex: The FH/MFSK signal has the following parameters:

No of bits per MFSK symbol, $k = 2$

No of MFSK tones, $M = 2^k = 2^2 = 4$

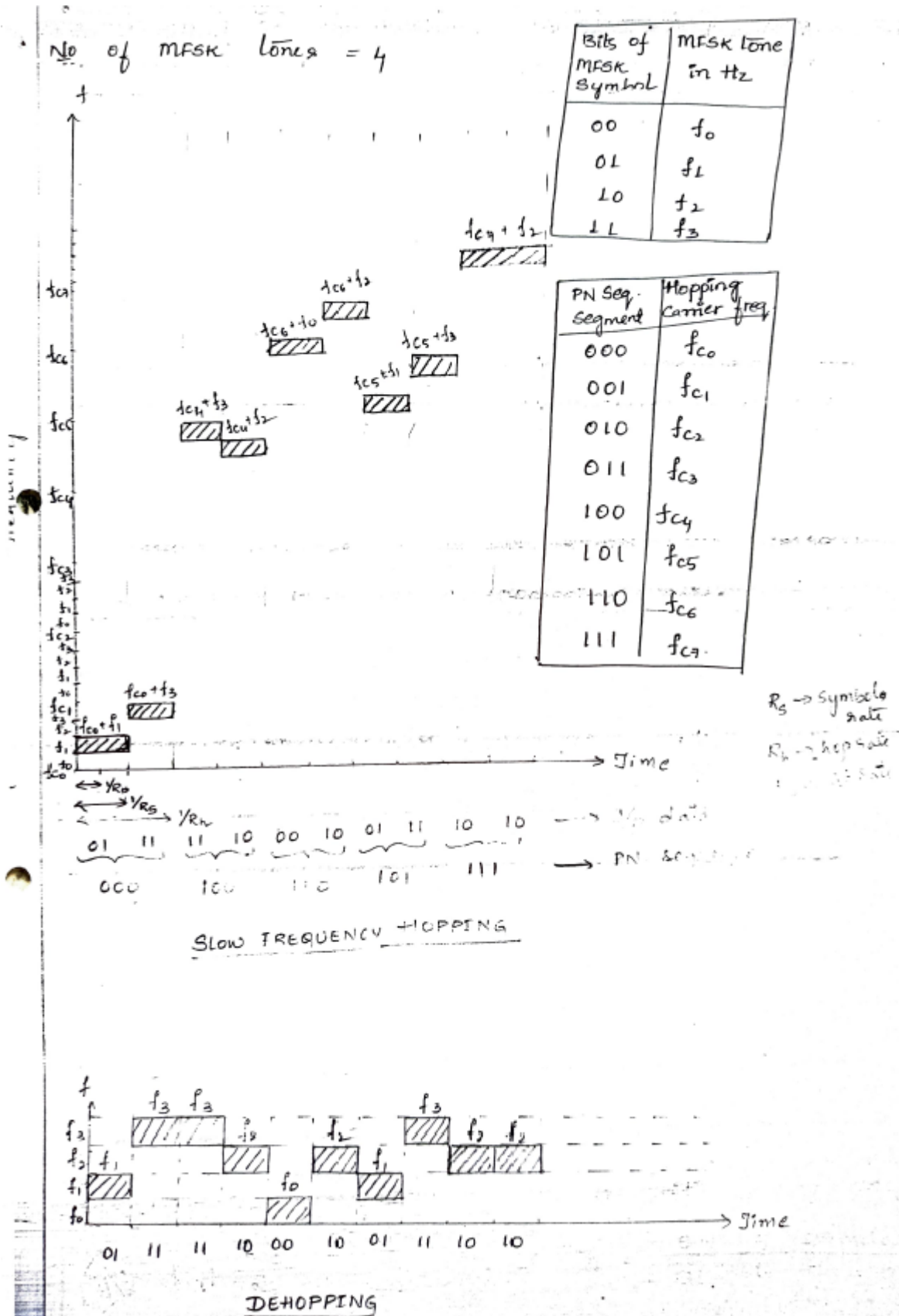
Length of PN segment per hop, $m = 3$

Total no of freq. hops = $2^m = 2^3 = 8$

Given data sequence is 01111110001001111010

& PN sequence is 000100110101111 [4-stage shift reg]

→ Solⁿ: Period of PN sequence, $N = 2^4 - 1 = 15$



Some Applications of DS Spread-Spectrum Signals

1. Low-detectability Signal Transmission:

- The aim is to hide the presence of the signal from receivers that are in the vicinity of the intended receiver.
- The signal is transmitted at very low power.

$$P_R/P_N \ll 1.$$

P_R Signal Power

P_N Noise Power

- The intended receiver can recover the weak information-bearing signal from the background noise using the processing gain and the coding gain.

2. Code Division Multiple Access:

- The type of digital communication in which each transmitter receiver user pair has its own distinct code for transmitting over a common channel bandwidth is called *code division multiple access (CDMA)*.
- If there are N_u simultaneous users, the desired signal-to-noise interference power ratio at a given receiver is:

$$\frac{P_S}{P_N} = \frac{P_S}{(N_u - 1)P_S} = \frac{1}{N_u - 1}$$

3. Communication Over Channels with Multipath:

- DS spread spectrum can generate a wideband signal for resolving multipath signal components.
- By separating the multipath components, we may also reduce the effects of fading.
- For example, in LOS communication systems where there is a direct path and a secondary propagation path resulting from signal reflecting from buildings and surrounding terrain, the demodulator at the receiver may synchronize to the direct-signal component and ignore the existence of the multipath component.

14

4. Wireless LANs:

- DSSS is used in IEEE wireless LAN standards 802.11 and 802.11b, which operates in the 2.4GHz frequency band.
- In the 802.11 standard, an 11 chip Barker sequence is used.

$$(1, -1, 1, 1, -1, 1, 1, 1, -1, -1, -1)$$

- With a BPSK modulation a data rate of 1Mbps is achieved.
- With a QPSK modulation a data rate of 2Mbps is achieved.

15

- In the second generation standard IEEE 802.11b, a data rate of 11Mbps is achieved.
- Complementary Code Keying (CCK), is used to encode data for 11Mbps.

Data rate	Code length	Modulation
1 Mbps	11 (Barker sequence)	BPSK
2 Mbps	11 (Barker sequence)	QPSK
5.5 Mbps	8 (CCK)	QPSK
11 Mbps	8 (CCK)	QPSK

CDMA based on IS-95

- With the help of CDMA it is possible to transmit many DS spread spectrum signals so that they can occupy same channel bandwidth. Each channel has its own PN sequence.
- CDMA is used widely for voice communication. It is standardized as IS-95. It uses the frequency band of 800 MHz TO 1900 MHz. CDMA has the advantage that the frequency reuse factor is 1.
- The bandwidth of 1.25 MHz is used for transmission from base station to mobile, on forward link. On separate channel, the bandwidth of 1.25 MHz is used on reverse link.
- The DS spread spectrum signals with CDMA are transmitted on forward and reverse link with the chip rate of 1.2288×10^6 chips/sec.

Forward Link (Base Station to Mobile Receiver)

Fig. 5.11.1 shows the block diagram of forward link.

- The speech encoder output is available at the rates of 9.6 kbps, 4.8 kbps, 2.4 kbps or 1.2 kbps. This data is encoded by convolutional encoder of rate 1/2 and constraint length of 9.
- The encoded data is then passed through a block interleaver. It overcomes the effects of burst errors. The output of interleaver is available at the rate of 19.2 kbps.
- The output of interleaver is scrambled by multiplication with long code. The long code generator has the chip rate of 1.2288 Mchips/sec. Its output is decimated to 19.2 kchips/sec.
- The Walsh sequence generator generates 64 bit long orthogonal Hadamard sequences. Each sequence is assigned to separate base station. There are 64 Hadamard sequences. Thus 64 channels are available for transmission.
- The Hadamard sequence multiplies with data sequences. Each encoded data bit is multiplied by the Hadamard sequence of length 64.
- The binary output sequence from multiplier is then multiplied with two PN sequences of length $N = 2^{15}$. It creates inphase and quadrature components.
- The I and Q components are passed through baseband shaping filters. The different base stations are identified by offsets of PN sequences.
- The I and Q DS spread signals are then modulated on two quadrature carriers and combined. This combined signal is then transmitted.
- The RAKE demodulator at the receiver is used to receive these signals. Viterbi soft decision decoder is used.

5.11.2 Reverse Link (Mobile to Base Station)

- Fig. 5.11.2 shows the block diagram of reverse link. The mobile transmitters are battery operated. Hence power efficient transmission is used.
- The speech encoder output is available at the rates of 9.6 kbps, 4.8 kbps, 2.4 kbps or 1.2 kbps. This data is encoded a convolutional encoder of rate 1/2 and constraint length of 9.
- Every 20 msec frame, 576 encoded bits are passed through block interleaver. Output of block interleaver is available at the rate of 28.8 kbps.
- The output of block interleaver is modulated with 64 orthogonal signal sets using Hadamard sequences each of length 64.
- The output of modulator has the bit rate of 307.2 kbps. The modulated signal is then randomized with the help of long code generator. It provides code at the rate of 1.2288 M chips/sec. Due to randomization, consecutive signal bursts and interference effects are reduced.
- The signal is then spread by two separate PN sequences over 'I' and 'Q' channels. The PN generators run at the rate of 1.2288 M chips/sec. Thus there are four PN chips for every bit of Hadamard sequence from modulator.
- The 'I' and 'Q' signals are then filtered by baseband spectral shaping filters. These signals then modulate two quadrature carriers. The modulated signals are finally added. This is nothing but offset QPSK.
- Computationally efficient Hadamard transform is used to reduce the computational complexity at demodulation. Viterbi decoder is then used to decode the signal.

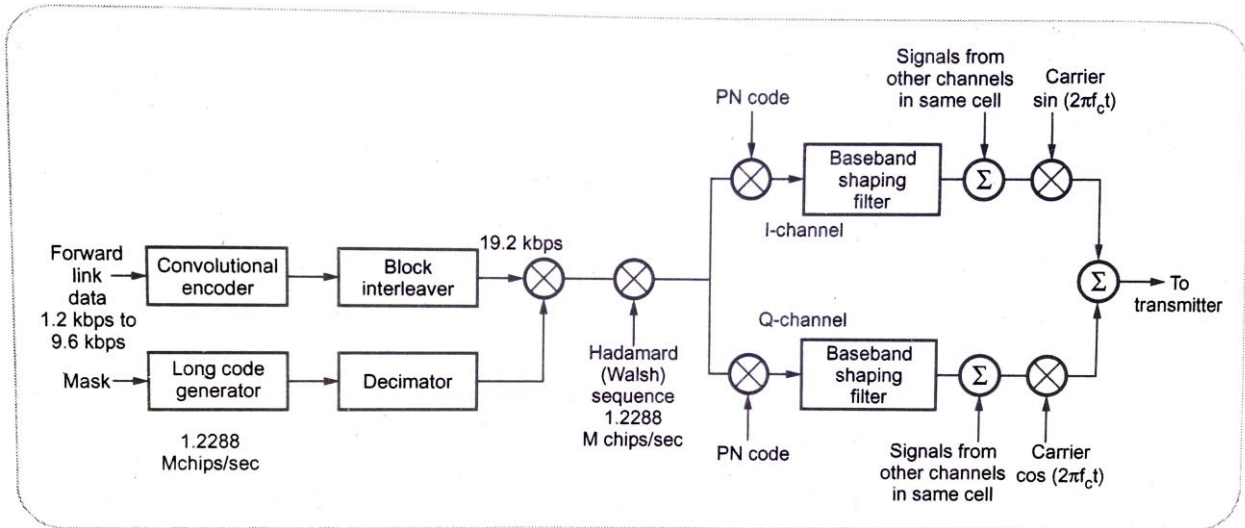


Fig. 5.11.1 Block diagram of forward link

Fig. 5.11.2

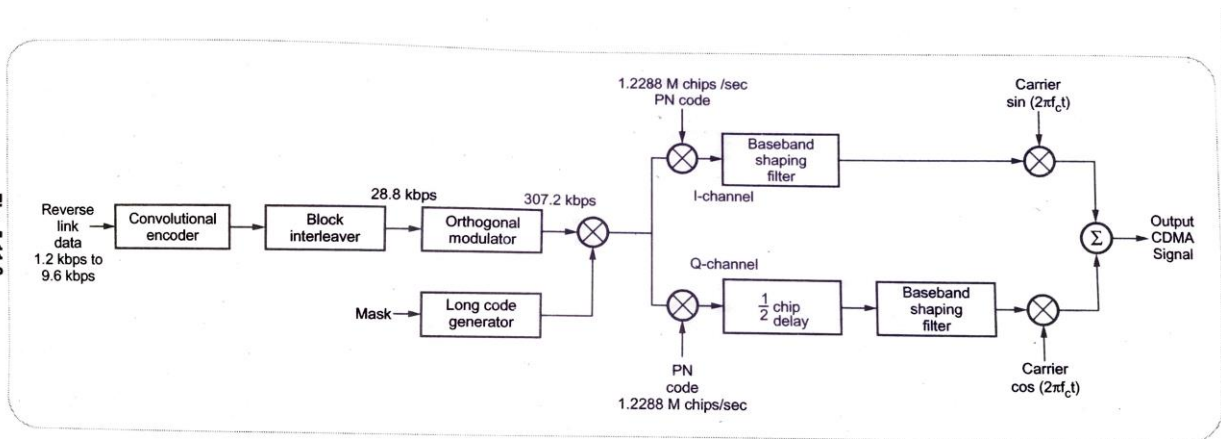


Figure 5.11.2 Block Diagram of reverse link